

CLAIMS

- 1 - Circuit integrated in a receiver system for digital television networks, characterized by the fact that it can process the flow of data from one or several MPEG data streams with different functions so that it adapts to receiver applications and to multimedia information servers (video, audio and data).
- 2 - The circuit that constitutes this invention is characterized by its versatility since it incorporates a single internal bus structure in which the transactions may be originated by the processor embedded into the circuit or by the peripherals.
- 3 - In addition, the circuit that constitutes this invention is characterized by the fact that it incorporates subsystems with routing and processing functions implemented by hardware and software, and used as modules that comprise the possible circuit configurations.
- 4 - The invention is characterized by a specific switching function between two or more conditional access systems. Even though it has been described for the case of 2 conditional accesses over 2 independent data streams, the current claim can be extrapolated to any number of conditional accesses and data streams.
- 5 - The invention is characterized by the fact that is it easily configurable, allowing at least the following functionalities: 1 - Processing of two input data streams to two output data streams; 2 - Processing of one input data stream (ITSINB) and one internally-synthesized data stream from the processor (PROC) to two output data streams; 3 - Processing of one input data stream (ITSINB), and one data stream from a local network access (ILAN), to two output data streams; 4 - Processing of two data streams, both from a local network connection (ILAN), to two output data streams; and 5 - Processing of two input data streams (ITSINA and ITSINB) to a local network concentrator/multiplexor.